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TRANSMITTAL LETTER

15535 U.S. PTO
10/643618
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The Commissioner for Patents
PO Box 1450
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ATTN: Mail Stop Patent Applications

Sir:

Transmitted herewith for filing is the patent application of: Jonathan S. Yedidia, Marc P.C. Fossorier, and Ravi Palanki.

FOR: BIT-FLIPPING DECODING OF ERROR-DECODING CODES BASED ON FINITE GEOMETRIES

Enclosed are:

- [30] pages of specification, claims and abstract
- [3] sheets of formal drawings
- [X] Declaration and Power of Attorney
- [X] Assignment of the application
- [X] Information Disclosure Statement

The filing fee is calculated as follows:

	NUMBER FILED	CLAIMS AS FILED NUMBER EXTRA	LARGE ENTITY	AMOUNT
Basic Fee				\$ 750.00
Total Claims	15	-20 =	x 18.00	\$ 0.00
Independent Claims	1	-3 =	\$80.00	\$.00
Assignment Recording Fee			40.00	\$ 40.00
Multiple Dependent Claim Fee			270.00	
Other				
TOTAL FEE				\$ 790.00

- [X] Please record the accompanying Assignment.
- [X] Please charge Deposit Account No. 50-0749 in the amount of \$ 790.00 for filing fees.
- [] Check in the amount of _____ for Filing Fee.
- [X] The Assistant Commissioner for Patents is hereby authorized to charge any additional fees due or credit any overpayment to Deposit Account No. 50-0749.

All future correspondence should be addressed to:

Patent Department
Mitsubishi Electric Research Laboratories, Inc.
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Cambridge, Massachusetts 02139

Duplicate copies of this letter are enclosed.

Respectfully submitted,
MITSUBISHI ELECTRIC RESEARCH LABORATORIES, INC.

By: 

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